

IN THE CLAIMS:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

Claim 1 (Currently Amended): A receiver system comprising:

a front end portion that receives and digitizes a data signal having one or more training tones and one or more data tones; and

a channel estimator component that utilizes the digitized data signal to provide a partial channel estimate that is combined with at least one other partial channel estimate to provide an aggregated channel estimate; and

a burst counter that increments a count value on receipt of an incoming data signal, the count value determining which partial channel estimate is updated;

wherein the channel estimator component is configured to extract training tones from the data signal and determine an average channel impulse response from the training tones, the average channel impulse response being utilized to determine the partial channel estimate.

Claim 2 (Original): The system of claim 1, the channel estimator component being adapted to update the aggregated channel estimate every K data signals, where K is the number of partial channel estimates forming a full channel estimate.

Claim 3 (Canceled).

Claim 4 (Currently Amended): The system of claim ~~[[3]]~~ 1, the count value being incremented modulo K, where K is the number of partial channel estimates forming a full channel estimate.

Claim 5 (Canceled).

Claim 6 (Currently Amended): The system of claim [[5]] 1, the channel estimator component further comprising a frequency shifter that shifts a spectrum of the average impulse response upon receipt of a data signal to provide N/K time samples to an N/K point Fast Fourier Transform component where N is the number of tones in a data signal and K is the number of partial channel estimates forming a full channel estimate, the N/K point Fast Fourier Transform component providing a partial channel estimate corresponding to the received data signal.

Claim 7 (Original): The system of claim 6, the frequency shifter shifting the spectrum of the average impulse response based on a count value that is incremented modulo K , such that a different partial channel estimate is determined based on the count value and a full channel estimate is updated every K data signals.

Claim 8 (Original): The system of claim 6, the frequency shifter shifting the spectrum of the average impulse response by multiplying the average impulse response by a frequency shift factor.

Claim 9 (Original): The system of claim 6, K being a power of 2.

Claim 10 (Original): The system of claim 1, the data signal being transmitted in a multicarrier modulation format.

Claim 11 (Original): The system of claim 1 being implemented as part of a modem.

Claim 12 (Original): The system of claim 1 being implemented as part of a wireless communication system coupled to the Internet.

Claim 13 (Currently Amended): A channel estimator comprising:

- a Fast Fourier Transform portion that receives a channel impulse response and performs a Fast Fourier Transform on the channel impulse response to provide a partial channel estimate; and
- a combiner that combines the partial channel estimate with at least one other partial channel estimate to provide an aggregated channel estimate, and ~~wherein the channel impulse response being determined using training tones embedded in a data burst and a zero pad component that adds zeroes to the channel impulse response until the channel impulse response has N/K number of time samples where N is the total number of tones in a data burst and K is the number of partial channel estimates that form a full channel estimate.~~
- a frequency shifter component that frequency shifts the spectrum of the channel impulse response upon receipt of a new data burst to provide a new set of N/K points to the Fast Fourier Transform portion where N is the total number of tones in a data signal and K is the number of partial channel estimates, such that a full channel estimate is updated every K data signals

Claim 14 (Canceled).

Claim 15 (Currently Amended): The channel estimator of claim 13, ~~further comprising a~~ wherein the frequency shifter component ~~[[that]]~~ frequency shifts the spectrum of the channel impulse response based on a burst count value that is incremented modulo K L when a new data burst is received, where K L is a reduction factor for the Fast Fourier Transform portion, such that the Fast Fourier Transform portion performs an N/K N/L point Fast Fourier Transform on the channel impulse response ~~where N is the total number of tones in a data burst.~~

Claim 16 (Currently Amended): The channel estimator of claim 13, wherein the channel impulse response being determined using training tones embedded in a data

burst and a zero pad component that adds zeroes to the channel impulse response until the channel impulse response has N/K number of time samples. further comprising a frequency shifter component that frequency shifts the spectrum of the channel impulse response upon receipt of a new data burst to provide a new set of N/K points to the Fast Fourier Transform portion where N is the total number of tones in a data signal and K is the number of partial channel estimates, such that a full channel estimate is updated every K data signals.

Claim 17 (Original): The channel estimator of claim 16, the shifter component comprising a frequency shift factor multiplier.

Claim 18 (Original): The channel estimator of claim 16, K being a power of 2.

Claim 19 (Original): The channel estimator of claim 16, further comprising a register bank having K tone set registers for storing channel estimate values based on different partial channel estimates, one of the K tone set registers being updated for each data signal that is received.

Claim 20 (Original): The channel estimator of claim 19, further comprising a multiplexer coupled to a burst counter that increments repeatedly between 0 and $K-1$ upon receipt of data bursts, such that the multiplexer updates one of the K tone set registers based on the state of the burst counter.

Claim 21 (Original): The channel estimator of claim 13 forming part of an application specific integrated circuit.

Claim 22 (Original): The channel estimator of claim 13 being implemented as executable instructions programmed in a digital signal processor.

Claim 23 (Previously Presented): A wireless communication system comprising:
an Inverse Fast Fourier Transform component operative to receive training tones
extracted from a digitized data signal and provide a channel impulse
response;
a frequency shifting component that shifts the spectrum of the channel impulse
response based on a burst count value;
a Fast Fourier Transform portion that receives the frequency shifted channel
impulse response and performs a Fast Fourier Transform on the
frequency shifted channel impulse response to provide a partial channel
estimate; and
a combiner that combines the partial channel estimate with at least one other
partial channel estimate to provide an aggregated channel estimate,
wherein the data signal being transmitted in a multicarrier modulation
format.

Claim 24 (Canceled).

Claim 25 (Currently Amended): A method for performing a channel estimation
procedure comprising: extracting training tones from a digitized data burst;
determining a channel impulse response based on the extracted training tones;
frequency shifting the spectrum of the channel impulse response; and
performing a Fast Fourier Transform on the frequency shifted channel impulse
response to provide a partial channel estimate; and
dividing the total number of tones N in a data burst into K sets such that $1/K$ of
the channel estimates are updated for each data burst received and a full
channel estimate is updated every K bursts.

Claim 26 (Original): The method of claim 25, further comprising combining the
partial channel estimate with partial channel estimates corresponding to previously
received digitized data bursts to provide an aggregated channel estimate.

Claim 27 (Original): The method of claim 25, the frequency shifting the spectrum of the channel impulse response comprising evaluating the expression:

$$h_{avg}(n) = h_{avg}(n) \cdot e^{j2\pi n b / N}; \quad n=0, \dots, s-1$$

where n is the sample number, s is the number of samples, b is a burst count value and N is the total number of tones in the digitized data burst.

Claim 28 (Original): The method of claim 25, the frequency shifting the spectrum of the impulse response comprising multiplying the channel impulse response by a frequency shift factor.

Claim 29 (Original): The method of claim 28, further comprising incrementing a burst counter having a burst counter value, the burst counter value changing the frequency shift factor multipliers for each data burst received, so that different partial channel estimate can be determined for each data burst.

Claim 30 (Original): The method of claim 29, the burst counter having a burst value being incremented modulo K for each data burst received, where K is the number of partial channel estimates that form a full channel estimate.

Claim 31 (Original): The method of claim 25, further comprising zero padding the frequency shifted channel impulse response to provide N/K number of time samples where N is the total number of tones in a data burst and K is the number of partial channel estimates that form a full channel estimate.

Claim 32 (Canceled).

Claim 33 (Original): The method of claim 25, the performing a Fast Fourier Transform on the frequency shifted channel impulse response comprising performing a N/K point Fast Fourier Transform where N is the total number of tones in a data burst and K is the number of partial channel estimates that form a full channel estimate.

Claim 34 (Previously Presented): A digital signal processor employing computer executable instructions for performing the method comprising:

- extracting training tones from a data burst;
- determining a channel impulse response based on the extracted training tones;
- frequency shifting the spectrum of the channel impulse response;
- performing a Fast Fourier Transform on the frequency shifted channel impulse response to provide a partial channel estimate;
- combining the partial channel estimate with at least one other partial estimate to form an aggregated channel estimate; and
- dividing the total number of tones N in a data burst into K training sets such that $1/K$ of the channel estimates are updated for each data burst received and a full channel estimate is updated every K bursts.

Claim 35 (Original): The digital signal processor of claim 34, further comprising combining the partial channel estimate with partial channel estimates corresponding to previously received data bursts to provide a full channel estimate.

Claim 36 (Canceled).

Claim 37 (Original): The digital signal processor of claim 34, the performing a Fast Fourier Transform on the frequency shifted channel impulse response comprising performing an N/K point Fast Fourier Transform where N is the total number of tones in a data burst and K is the number of partial channel estimates that form a full channel estimate.

Claim 38 (Previously Presented): A system for determining the effects of a transmission environment on a transmission signal, the system comprising:

- means for extracting training tones from a received data signal;
- means for determining a channel impulse response based on the extracted training tones;
- means for frequency shifting the spectrum of the channel impulse response;
 - means for performing a Fast Fourier Transform on the frequency shifted channel impulse response to provide a partial channel estimate; and
- means for combining the partial channel estimate with other partial channel estimates determined for different data signals to provided an aggregated channel estimate; and
- means for dividing the total number of tones N in a data burst into K training sets such that $1/K$ th of the channel estimates are updated for each data burst received and a full channel estimate is updated every K bursts.

Claim 39 (Canceled).

Claim 40 (Currently Amended): A method for performing a channel estimation procedure comprising:

- dividing a total number of tones N in each one of a plurality of data bursts into K sets;
- determining a channel impulse response of $1/K$ th of the channel for each data burst; ~~and~~
- providing a full channel estimate every K bursts;
- frequency shifting spectrum of the channel impulse response; and
- performing a Fast Fourier Transform on the frequency shifted channel impulse response to provide a partial channel estimate.

Claim 41 (Canceled).

Claim 42 (Currently Amended): A communication device comprising:
means for dividing a total number of tones N in each one of a plurality of data bursts into K sets;
means for determining a channel impulse response of $1/K$ th of the channel for each data burst; [[and]]
means for providing a full channel estimate every K bursts;
means for frequency shifting spectrum of the channel impulse response; and
means for performing a Fast Fourier Transform on the frequency shifted channel impulse response to provide a partial channel estimate.

Claim 43 (Canceled).